



B.M.S. College of Engineering
Bengaluru-560 019

Two-week online Value Added
course on

VLSI , VERILOG &
IMAGE PROCESSING

June 6th to 18th 2022

Organized by

Dept. of Electrical and Electronics Engg.
BMS College of Engineering
Bengaluru-560019

in collaboration with
EEE Association(EEEA)

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Dr. Pawan Dubey, MITS, Gwalior
Dr. Nidhi Saxena, MITS, Gwalior

WHO CAN ATTEND

Students and research scholars from
BMSCE and other institutes

IMPORTANT DATES

Last date for registration is 06/06/2022,
10AM.

REGISTRATION FEE

₹ 400/=

Payment details in registration form.

HOW TO APPLY

Use the link below to register:

<https://forms.gle/fCyGzzDcumLej4uz6>

TIMINGS

5:00 pm to 8:00 pm
E-certificates will be provided
to all active participants.

IN CASE OF ANY QUERIES,

Contact:

1. Likhita: 9742200158
2. Sangam: 7301643467
3. Aditya: 9836850789

or send us an email at:

eeea@bmsce.ac.in

ABOUT THE COURSE

This Value-Added Course throws light on promising areas such as VLSI, Verilog and Image Processing and its diverse applications. This is a two-week course, which has equal importance on VLSI and Verilog in the first half and Image Processing on the second. The VLSI week starts with Introduction of VLSI and Verilog which then progresses with topics such as ASIC Design Flow, Finite State Machine and ends with a talk on industry application of VLSI. The second week starts with introduction to Image processing, and continues with its processing like encryption, decryption and ends with the research trends like usage of Image Processing and its tools in the Industry and other related areas.

OBJECTIVES OF THE COURSE

To introduce the concepts of VLSI and image processing. The course familiarizes the participants with modeling and RTL coding using verilog HDL which is applied in chip design and verification and implementation of digital systems including finite state machines. The course also deals with the basic analytical methods to be used in image processing. To familiarize students with image enhancement and restoration techniques. To explain different image compression techniques. To introduce segmentation, encryption, decryption, and image enhancement through Frequency and Spatial Domain.

OUTCOMES

At the end of the course, the students will be able to:

1. Apply the back-end physical flow, including floor planning, placement and routing techniques
2. Gain knowledge on ASICS and FPGA, standard cells, cell library, IPs
3. Learn about image compression, segmentation, restoration, encryption, decryption, and object recognition.
4. Implement image processing tools on Noise reduction, detection of discontinuities, recognition of patterns, etc.

B.M.S. COLLEGE OF ENGINEERING

BMS College of Engineering was founded in the year 1946 by Late B M Sreenivasaiah, and nurtured by his illustrious son, Late B S Narayan. BMSCE is the first private sector initiative in Engineering Education in India. BMSCE is in the field of Engineering Education with over 7 decades of dedicated service. BMSCE today offers 14 Under Graduate & 15 Post Graduate courses, both in conventional and emerging areas. At present, 15 Departments are recognized as Research Centers offering PhD/MSc Engineering by Research in Science, Engineering, Architecture and Management. First Institution in the state bestowed with NBA Accreditation in Tier I Format (Washington Accord). The World Bank funded

TEQIP Phase I, Phase II and Phase III Institute and Partner Institution of the Melton Foundation, USA which advocates Global Citizenship. Autonomous Institute (UGC approved) since 2008.

DEPARTMENT OF EEE

The Department of Electrical Engineering was established in 1946 and subsequently emerged as the Department of Electrical and Electronics Engineering. The Department has the distinction of having its graduates known worldwide for their contributions and accomplishments in varied domains.

EEE Association

The EEEA creates an environment to enable the development of a student in terms of their leadership and interpersonal skills. It is an integral part of the Electrical & Electronics Engineering Department. The association is a stepping stone to conduct brainstorming sessions to discuss and propose solutions for various research problems in the field of Electrical Engineering and drive towards the betterment of engineering education. The EEA will be a major contributor towards organizing EEE technical fests, hobby clubs, social service and various techno-cultural events. The EEA is a student friendly association and will always aim at promoting the interests of the students and provide financial support to the needy students. Through the BMSCE EEA students are encouraged to pursue higher education, create projects, write papers, and much more.